XA-9989 PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hideyuki MATSUOKA et al.

Appln. No.:

Group Art Unit:

Filed: HEREWITH

For: MAGNETIC SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

Document AP on the attached List was cited in the specification, on page 1, and its relevance is indicated therein. Document AA is the U.S. counterpart of Document AJ.

Respectfully submitted,

Mitchell W. Shapiro

MWS:lat

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Reg. No. 31,568

November 19, 2003

FORM PTO-1449					Atty. Docket	No.	Appl	.n. No.
					XA-9989			
LIST OF DOCUMENTS CITED BY APPLICANT								
					Applicant			
					Hideyuki MATSUOKA et al.			
					Filing Date			ıp
					HEREWITH			
U.S. PATENT DOCUMENTS								
Examiner Initial		Document Number	Date	Name		Class	Sub- class	Filing Date
	AA	2002-0093845	7/18/02	Matsuoka	et al.	365	97	
	AB			-				
	AC							
<u>-</u>	AD							
	AE							
	AF							
	AG							
	АН							
	ΑI							
FOREIGN PATENT DOCUMENTS								
xaminer nitial		Document Number	Date	Со	untry	Class	Sub- class	Translation
	AJ	2002-208682	7/26/02	Japan				Abstract
	AK			-				
	AL							
	AM					-		
	AN							
	AO							
OTHER (including author, title, date, pertinent pages, etc.)								
	AP	Durlam et al., "A low power 1Mbit MRAM based on 1T1MTJ bit cell integrated with Copper Interconnects," 2002 Symposium on VLSI Circuits Digest of Technical Papers, pp. 160-163.						
	AQ			···				,
	AR							
.xaminer Date Considered								
.XAMINER: Initial if reference considered, whether or not citation is in conformance								

XAMINER: Initial if reference considered, whether or not citation is in conformance ith MPEP 609; draw line through citation if not in conformance and not considered. nclude copy of this form with next communication to Applicant.